

For  $\pm 5V$  supplies:

Change Single-ended input resistance for AD8146/AD8147 from 1.13k $\Omega$  to 750 $\Omega$ .

Change Single-ended input resistance for AD8148 from 1.13k $\Omega$  to 833 $\Omega$ .

Change the minimum Differential output gain limit for AD8146/AD8147 from 1.95 to 1.94V/V.

Change Differential output gain for AD8148; minimum from -3.42 to +3.8V/V, maximum from +3.5 to 4.0V/V.

Change the minimum Output Common Mode (OCM)DC gain limit for AD8146 from 0.986 to 0.98V/V.

Change OCM Input offset voltage for AD8146 from  $\pm 20mV$  to  $\pm 36mV$ .

Change OCM DC Common Mode Rejection Ratio (CMRR) for AD8146 from a maximum to a typical value (-48dB).

Change the maximum Gain to red common-mode output limit for AD8147 from 1.08 to 1.10V/V.

Change Gain to red common-mode output limit for AD8148; minimum from 0.965 to 0.96V/V, maximum from 1.04 to 1.05V/V.

Change the maximum Gain to green common-mode output limit for AD8147 from 2.11 to 2.15V/V.

Change Gain to green common-mode output limit for AD8148; minimum from 1.935 to 1.93V/V, maximum from 2.05 to 2.08V/V.

Change the minimum Gain to blue common-mode output limit for AD8147 from -1.08 to -1.10V/V.

Change Gain to blue common-mode output limit for AD8148; minimum from -1.035 to -1.05V/V, maximum from -0.965 to -0.96V/V.

Change Quiescent current for AD8146 from  $\pm 57m$  to  $\pm 58mA$ .

Remove minimum Output voltage when OPD Asserted limit.

For 5V or  $\pm 2.5V$  supplies,

Change Single-ended input resistance for AD8146/AD8147 from 1.13k $\Omega$  to 750 $\Omega$ .

Change Single-ended input resistance for AD8148 from 1.13k $\Omega$  to 833 $\Omega$ .

Change Differential input DC CMRR for AD8146/AD8147/AD8148 from -53/-49/-55 to -49/-45/-49dB.

Change Differential output gain for AD8146/AD8147; minimum from 1.95 to 1.94V/V, maximum from 2.013 to 2.00V/V.

Change the minimum Differential output gain limit for AD8148 from 3.87 to +3.8V/V.

Change Output voltage swing for AD8148; minimum from -1.23 to -1.17V, maximum from 1.26 to 1.24V.

Change the minimum OCM DC gain limit for AD8146 from 0.99 to 0.98V/V.

Change OCM Input offset voltage for AD8146 from  $\pm 20m$  to  $\pm 36mV$ .

Change Gain to red common-mode output for AD8148; minimum from 0.925 to 0.925V/V, maximum from 1.0 to 1.04V/V.

Change the maximum Gain to green common-mode output limit for AD8147 from 2.05 to 2.08V/V.

Change Gain to blue common-mode output for AD8148; minimum from -1.0 to -1.04V/V, maximum from -0.925 to -0.92V/V.

Change Quiescent current-positive supply for AD8148 from 53m to 54mA.

Change Quiescent current-negative supply for AD8148 from -52m to -53mA.

Change Power Supply Rejection Ratio (PSRR) for AD8147 from -54 to -52dB.

Remove minimum Output voltage when OPD Asserted limit.